



Computing at JBCA  
Dr Anthony Holloway

# Computing at JBCA

- Development
- Current Facilities
- Future Upgrades

# Lovell Telescope Upgrade



2000

# Lovell Telescope Upgrade



# Lovell Telescope Upgrade



# Lovell Telescope Upgrade



2002

# Lovell Telescope Upgrade



2002

# Control Room





# Mk II

- Controlled by  
Ferranti Argus 100



# Micro Circe



# Micro Circe





1980-2005





2006+ JACH

TOPCAT, GAIA, ORAC-DR,  
KAPPA, CCDPACK etc

GAIA: Polarimetry (/loc/pola/pdraper/bd31/bdb/temp.FIT)

GAIA::Skycat: bdb\_WCS\_r.sdf (1)

File Edit Options File View Graphics Go Image-Analysis Data-Servers Help

Object: bdb\_WCS\_r.sdf

X: 215.5 Y: 297.0 Value: 559.664

$\alpha$ : 215.0  $\delta$ : 296.5 Equinox:

Min: -25.4609 Max: 120488 Auto Cut:

Low: -15.625 High: 1286.3 Color Map:

Scale: 2x      Intensity Map:

Zoom

PI

3919	56.8563
5516	37.6399
9884	45.9308
9449	67.9071
1009	78.7362
2329	73.2467
2674	61.8297
9024	51.1879

Zoom

**Polarisation Map**

Decination

Right ascension

image: = select object, = scroll image, = measure WCS, Control + = select region

# Desktop Infrastructure

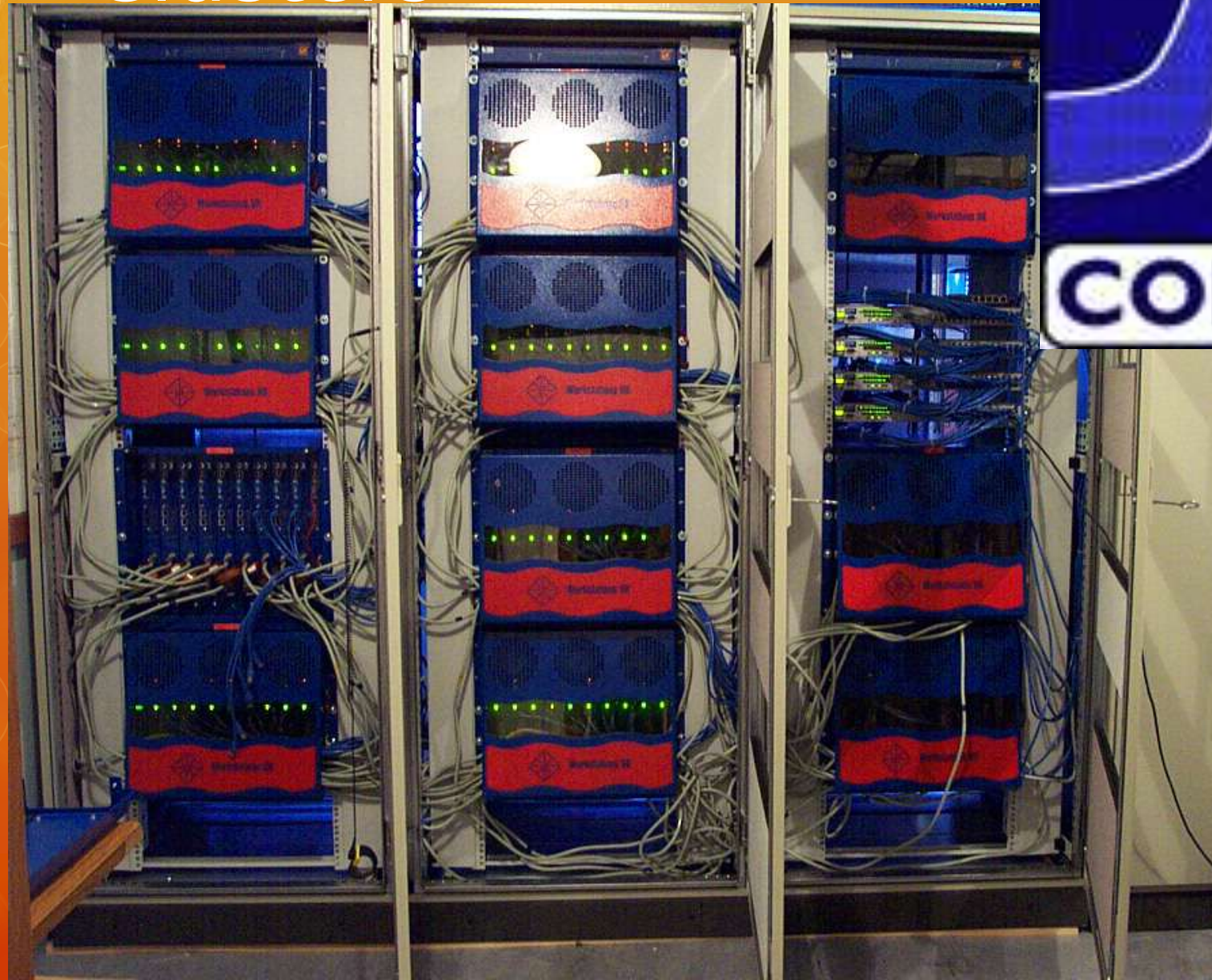
- Starlink
- AIPS
- Casa
- IRAF / STSDAS
- IDL
- MATLAB
- Mathematica
- Intel C
- Intel Fortran
- Boa
- Gildas
- Healpix
- Miriad
- Python
- Sched
- SuperMongo
- Latex
- Libraries

# Server Infrastructure

- Central storage
- ~ 15 Websites
- ~ 40 Wikis
- E-mail
- Mailing Lists
- Databases
- Archives
  
- Clusters



# Clusters

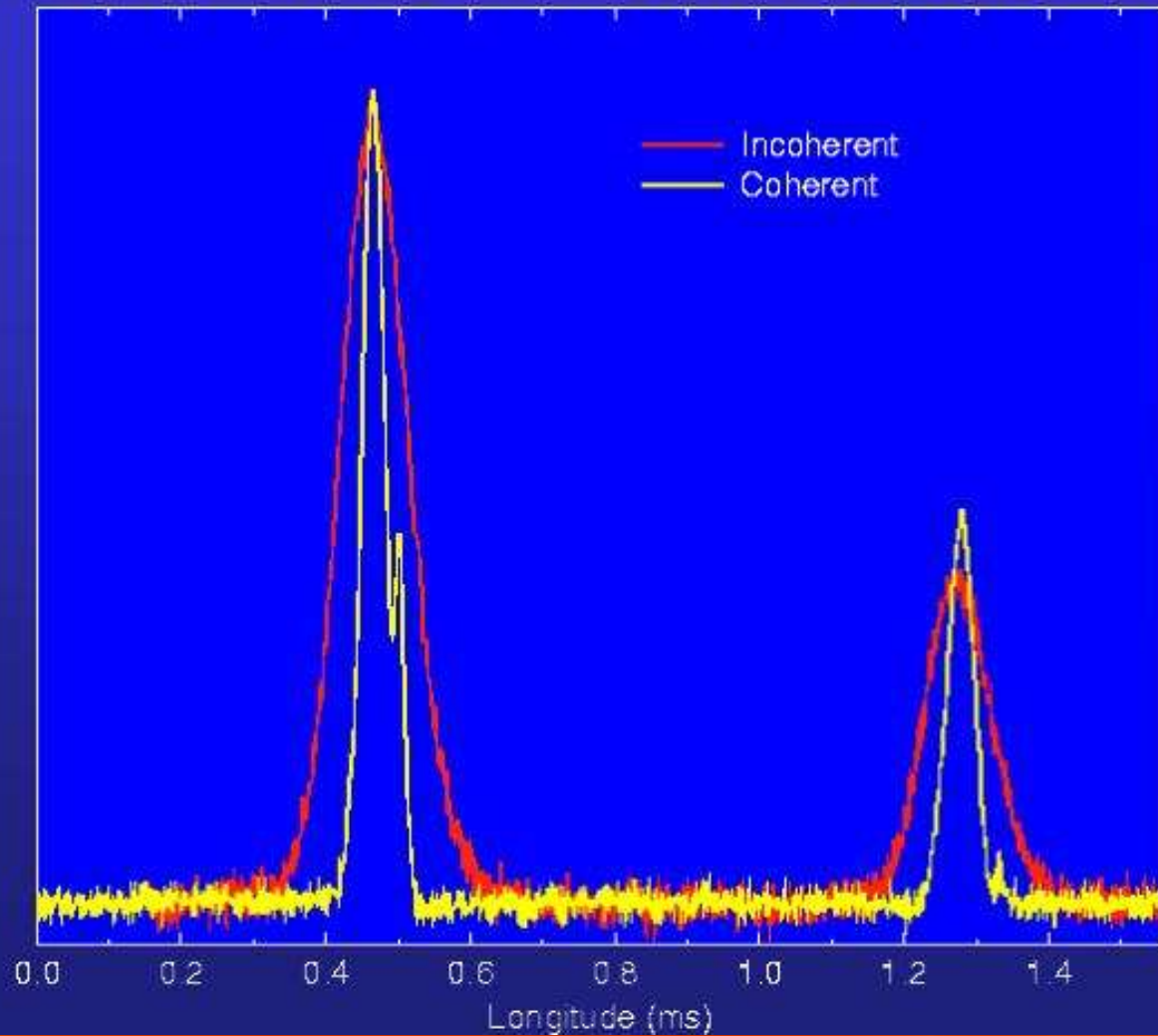




- 182 x 1.13 GHz Pentium III CPU's
- 91 Dual CPU Tyan Thunder Motherboards
- 91 x 20 GB Disks (= 1.82 TB)
- 41 x 512 MB RAM  
40 x 1024 MB RAM (= 60 GB)
- 90 x SCI High Speed Interconnect (320 MB/s)
- 91 x Fast Ethernet (12.5 MB/s)
- 1.05 TB Bulkserver

~500<sup>th</sup> Fastest Computer in the World!!

# Incoherent vs. Coherent De-dispersion

















# Hydra & Coma



# Hydra, Adder, Jumper & Coma

## Hydra/Adder/Jumper :-

182 nodes

2x Quad Core Intel Xeon 2.66 GHz E5430 CPU

4 – 16 GB RAM

1456 cores

## COMA :-

45 nodes

Mix of 2 x Quad Core Xeon 2.26 GHz E5520 with Hyper-Threading

And Pentium Dual Core 3 GHz

2 – 24 GB RAM

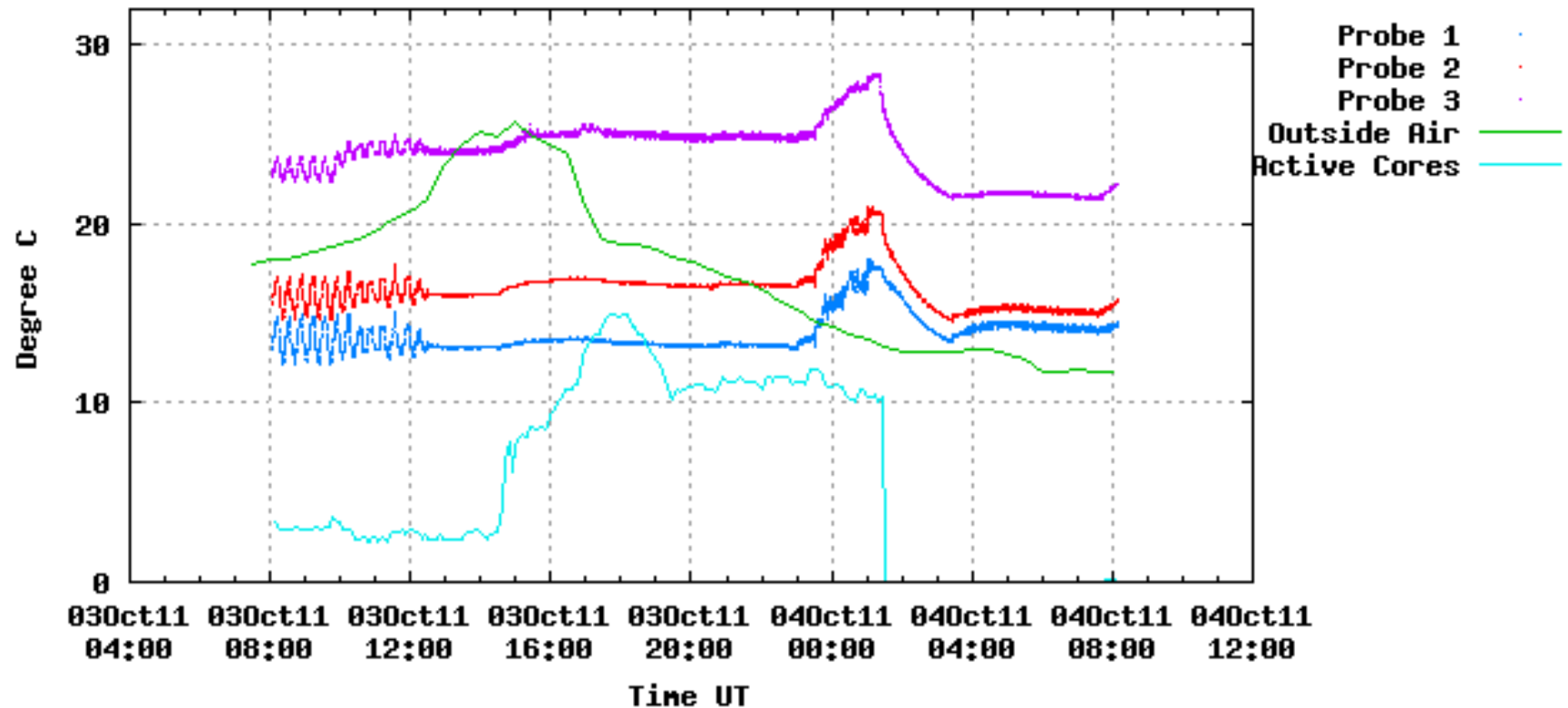
328 virtual cores

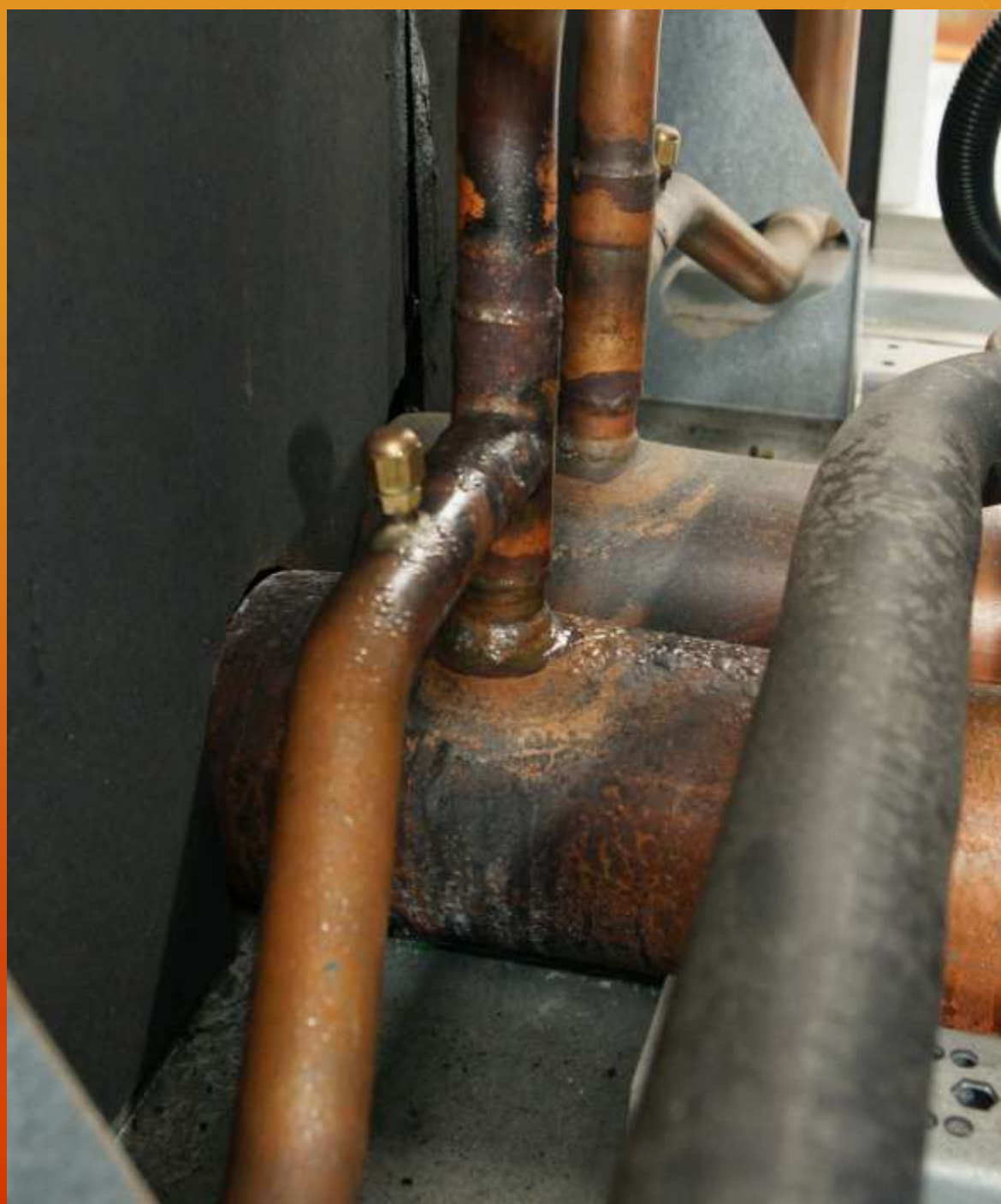
# Hydra, Adder, Jumper & Coma

- Hydra :- Searching for Pulsars
- Jumper:- LEAP data acquisition
- Adder :- LOFAR Pulsars, LEAP processing, Pulsar searching
- Coma:- Theory & Cosmology – Solar Physics, CMB, Planck, QUIET, Stellar, Radiative Transfer, Galaxy Formation & Evolution



Temperature in Turing Processor Rack past 24 hours





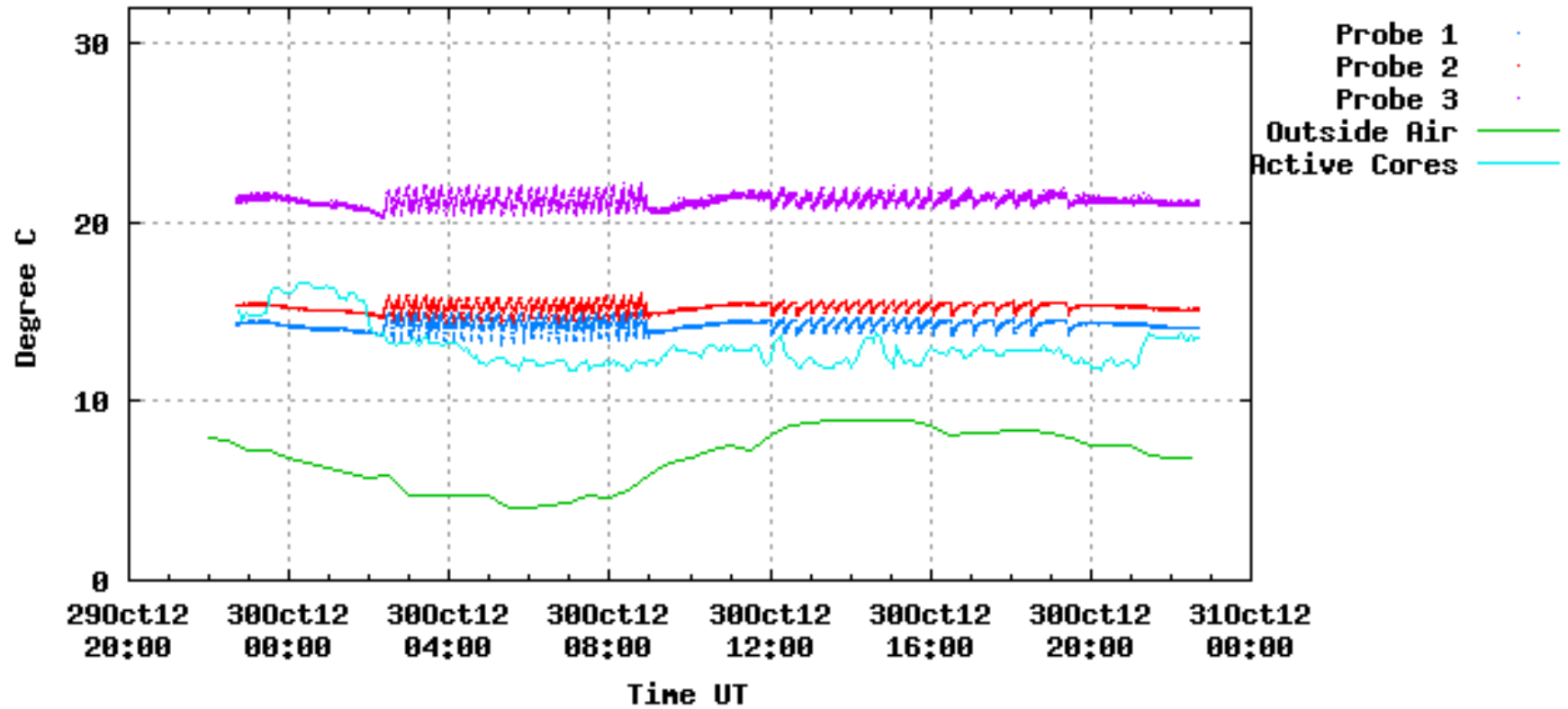








Temperature in Turing Processor Rack past 24 hours



# What can you achieve?

- Embarrassingly parallel jobs scale with  $N_{\text{cpu}}$  (nearly)
- Serial codes made parallel will depend on the code
- Speed up is defined by Amdahl's Law

# Amdahl's Law

- $S$  is fraction of the code that is serial
- $1-S$  is fraction that can be parallelized
  
- The speed up using  $P$  processors is

$$\frac{R(P)}{R(1)} = \frac{1}{S + \frac{1-S}{P}}$$

# Not that simple

- Interprocess communication delay ( $T_c$ )
- Extra code in serial section to setup parallel parts (once per process) ( $T_{is}$ )
- Extra code in parallel section (once per process but occurs in parallel) ( $T_{ip}$ )
- Code specific communications e.g.  $P(P-1)*T_c$

$$\frac{R(P)}{R(1)} = \frac{T_s + T_p}{T_s + PT_{is} + \frac{T_p}{P} + T_{ip} + P(P-1)T_c}$$

# How to go about it

- Look at your code – what can be done in parallel?
- How much time is spent doing it?
- Use Amdahl's Law – is it worth it?
- Extra code adds delays - setup, comms, barriers
- Is there a parallel algorithm to replace the serial one?
- How well will the code scale as you increase the number of nodes being used?

# How to go about it

- Amdahl's Law – Law of diminishing returns
- You can divide a problem so small that with many CPU's it actually runs slower

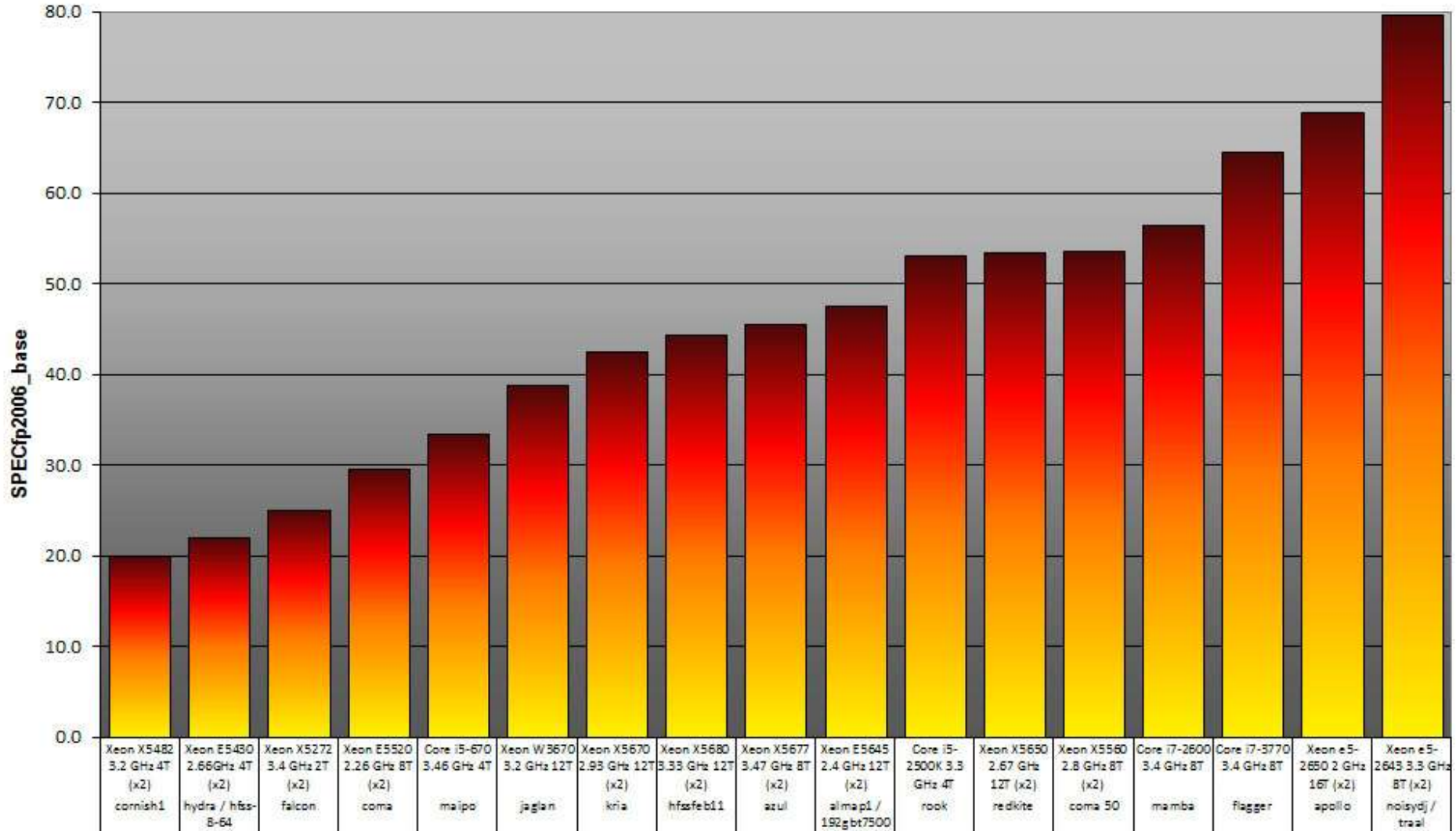


# Message Passing Interface (MPI)

- Library of routines for C/Fortran
- MPICH for Ethernet
- E.g. MPI\_Send, MPI\_Recv
- Runs on a set of nodes at start up
  
- IT Services (Research Computing Services) run courses on MPI
- 29<sup>th</sup> November
- Materials on-line

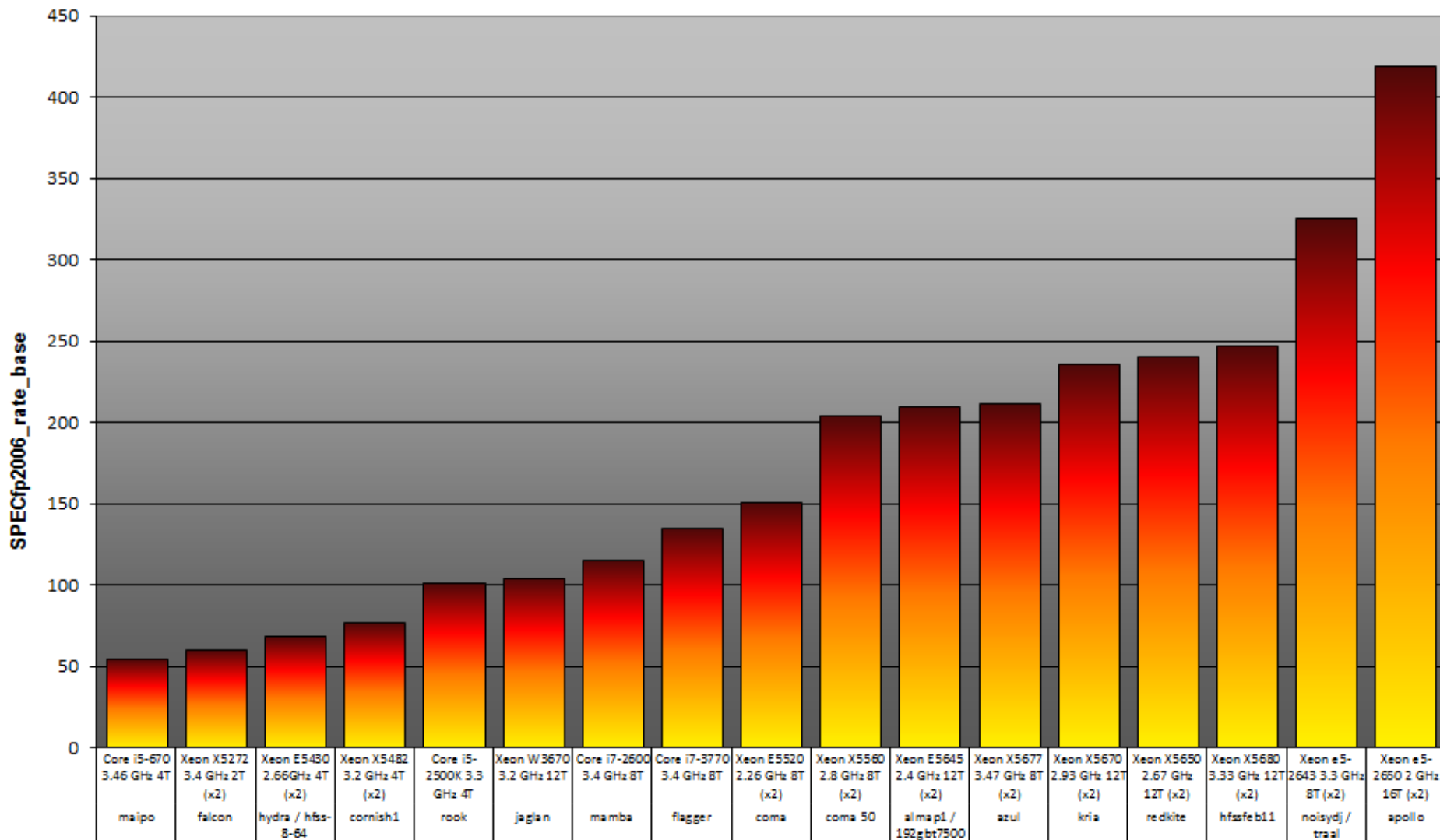
# Machine Performance

CPU Floating Point Benchmark - 1 parallel multi-core job - July 2012



# Machine Performance

CPU Throughput Floating Point Benchmark - 'n' jobs on 'n' threads- July 2012

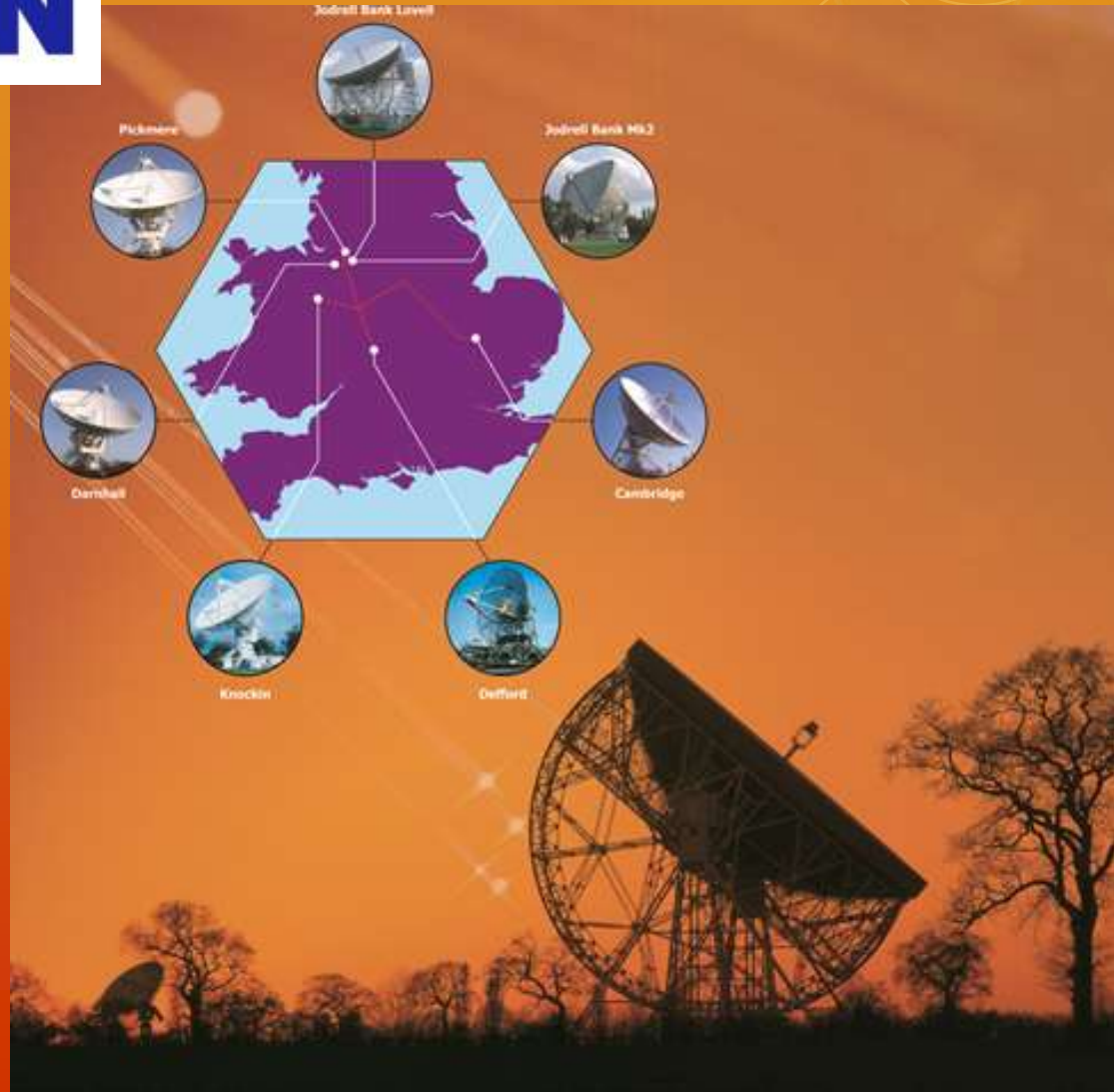


# GPU's

- Geforce – Galaxy Simulation
- Navi – Pulsars
- Apollo - Pulsars
- New PC – Optic Fibre Transmission

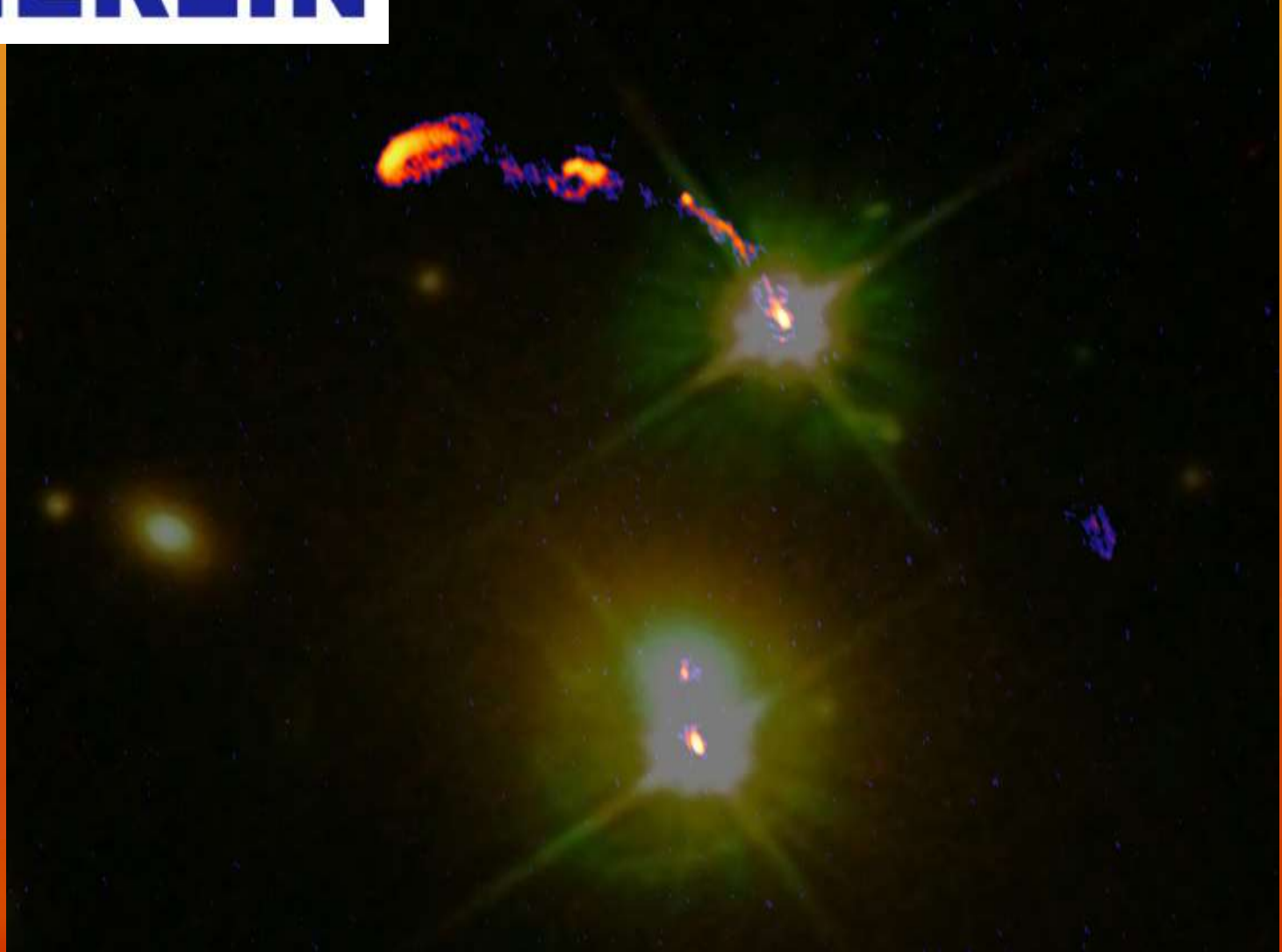


**Fibre Links**  
**Wide Bandwidth**  
**= 0.5 TB a day**









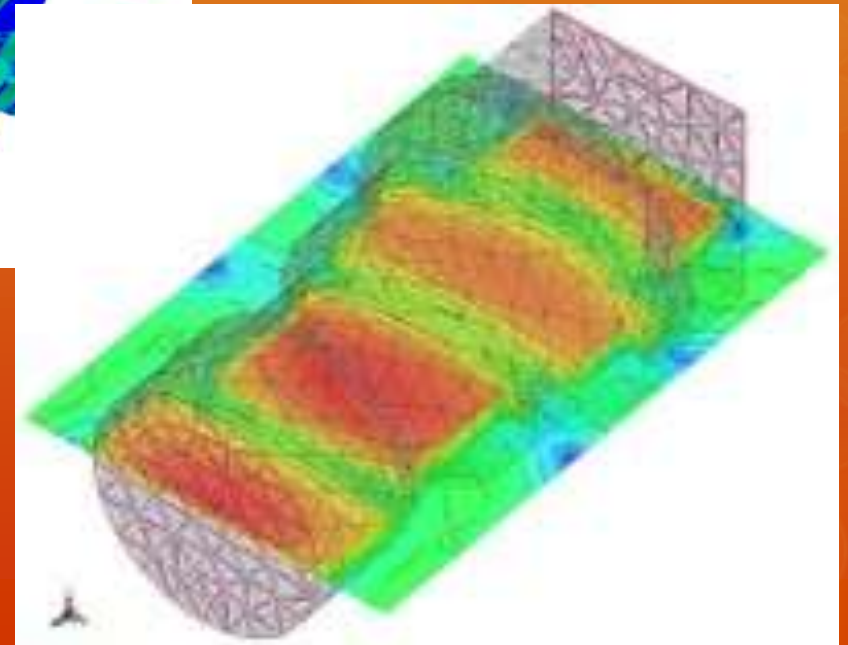
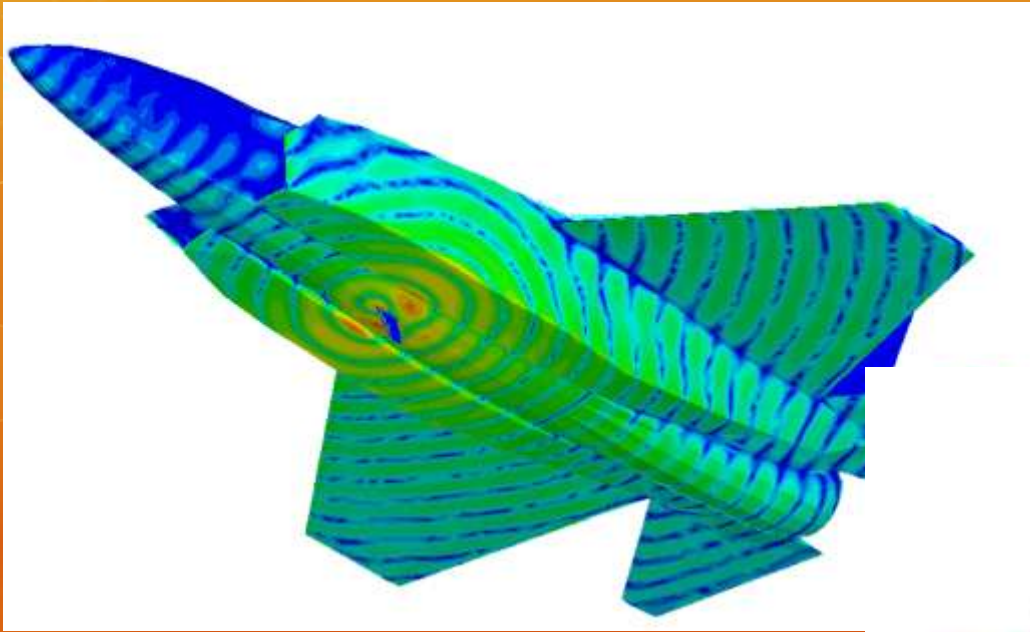


The University of Manchester  
Jodrell Bank  
Observatory

MANCHESTER  
1824

# ALMA ARC

# Technology



HFSS

FEKO

GRASP

# LOFAR Data Analysis



- Dedicated 1 Gbps Fibre Link to Netherlands



# International Virtual Observatory Alliance



# Aladin

**Aladin v3.0 multiview**

Load... Save... Tools... Print... Help... Quit

Position: J2000 18:02:31.33 -22:57:55.5 Pixel: 8 bits 162/145/055

Trifid nebula

Tools: selec, dist, draw, tag, text, filter, rgb, blink, isamp, zoom, mgls, hist, prop, del

Layers: Circ.Mag, HST, USNO-B1, **RGB img**, SERC.S.MAN, SERC.I.MAM, SERC.V.DSS

- RGB img Zoom: 1/2x

Image	PCimage	M20-TC1	18 02 26.57	-23 01 20.6	WFPC2	548.3							
Image	PCimage	M20-TC1	18 02 26.57	-23 01 20.6	WFPC2	548.3							
Association(CADC1)	270.5885	-23.0330	2001-07-05	T12:30:14	600.0	HD164492A							
0669-0683137	270.586123	-23.039303	1964.9	0	0	2	9.84				10.99	0	0
0669-0683138	270.596381	-23.033381	2000.0	0	-12	0	8.64	8.66	8.65	8.66	8.68	0	0

(c)1999-2005 ULP/CNRS - Centre de Données astronomiques de Strasbourg 7 planes, 1 view, 20Mb

# Video Conferencing - AccessGrid



# Imaging, Audio & Video Production



# Imaging, Audio & Video Production

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LATEST AUDIO > [IMC 2012](#) | LATEST VIDEO > [LOFAR](#)

## LOFAR

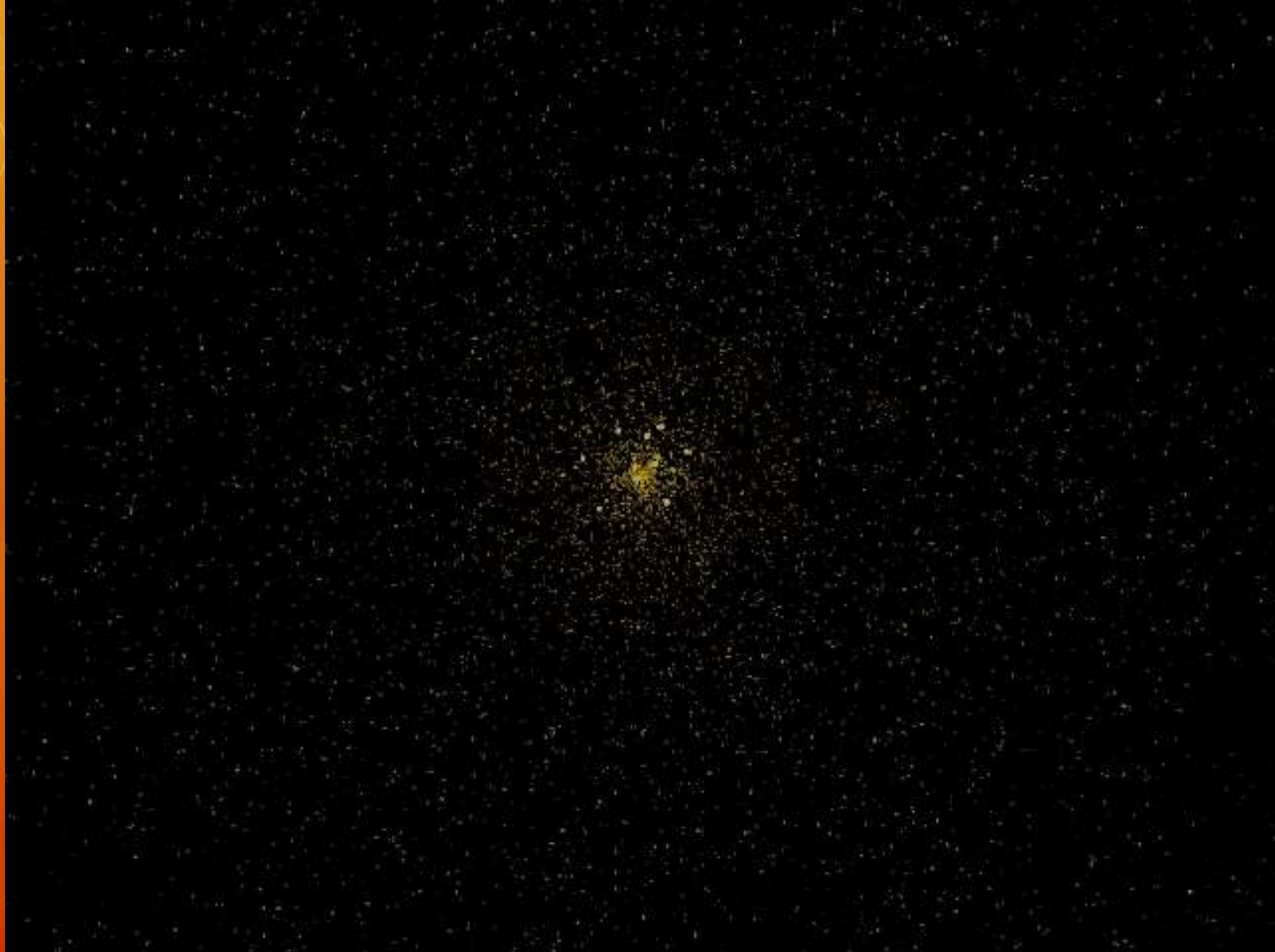
Release date: Sunday, 14th October 2012



CREDIT: STFC/SEPnet



# Imaging, Audio & Video Production



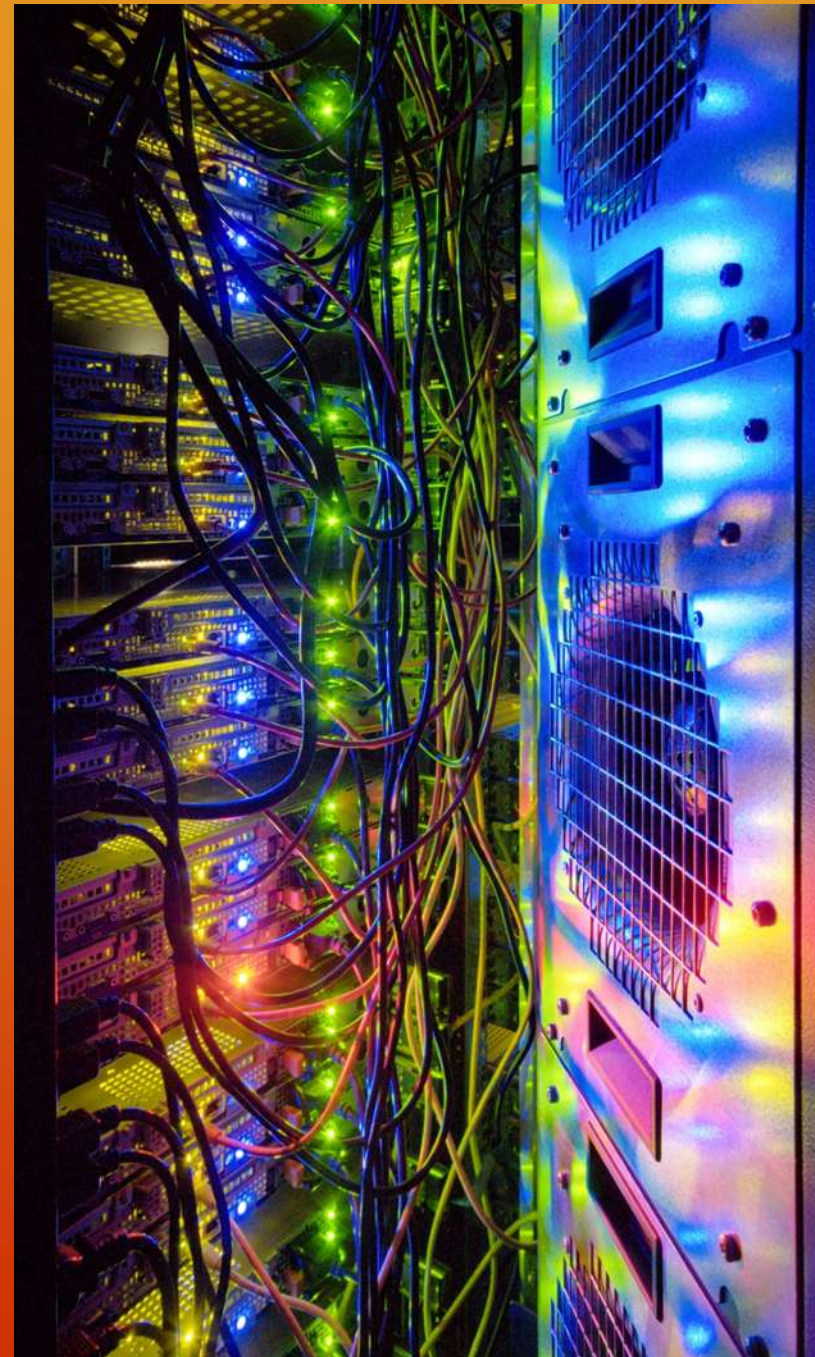
## **New Developments:-**

**Star2**

**ALMA cluster**

**COMA upgrades**

**HYDRA upgrade**





Any Questions?